

## PATENT ABSTRACTS OF JAPAN

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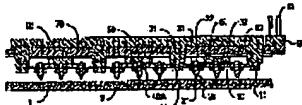
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### (54) MEASURING ELECTRODE FOR INTEGRATED CIRCUIT

#### (57)Abstract:

**PURPOSE:** To highly accurately bring contacts into contact with pads on a pattern relevant to an individual IC part by a method wherein a conductive and buffer member electrically connects base lands with opposed lands and moves a contact block in a contact direction toward a relay substrate.

**CONSTITUTION:** A conductive and buffer member 50 electrically connects base lands 32 relevant to a base 10 and a relay substrate 60 with opposite lands 61 for performing electrical connection and buffering. The member 50 moves a contact block 20 provided on the base 10 toward the relay substrate 60 in a contact direction of pads 2 of an object to be examined. This movement regulates a position with fitting of a guide boss 72 for alignment provided on a support base 70, a guide boss 72 for alignment provided on the base 10 and a semicircular recess 11 provided on the base 10. Thus the contact block 20 can be highly accurately brought into contact with the pads 2 on a pattern concerned with an individual IC part.



#### LEGAL STATUS

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